PTO/SB/08a

SEP 06 2005

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Atty. Docket No.	Serial No.
1410-794	10/628,578

Applicant	
LÜPPERT et al	

Filing Date	TC/A.U.
July 29, 2003	2128

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

T.P.	Sommer et al, "Analog Insydes 2 - New Features and Applications in Circuit Design"
T.P.	Halfman et al, "Behavioral Modeling and Transient Analysis With Analog Insydes"
T.P.	Sommer et al, "A Generic Circuit Modeling Strategy Combining Symbolic and Numeric Analysis", ITWM (Institute of Industrial Mathematics, Kaiserslautern, Germany. 1998
T.P.	Halfmann et al, "A Numerical Method to Synthesize the Element Characteristic in Analog Circuit Design"
T.P.	Halfmann et al, "Application of Symbolic Circuit Analysis: An Overview and Recent Results in Nonlinear Model Generation"
T.P.	Lutovac et al, "A MATLAB Toolbox for Analysis of Continuous-Time Filters", PROC 22nd International Conference on Microelectronics (MIEL 2000), Vol. 2, NIS, SERBIA, 14-17 May 2000
T.P.	Lutovac et al, "AFDESIGN - A Toolbox for Continuous-Time Filter Design in MATLAB"
T.P.	Lutovac et al, "Symbolic Analysis of Programmable Digital Filters"
T.P.	Lutovac et al, "Digital Filter Design Using MATLAB and Mathematics"
T.P.	Tošić et al, "A Tool for Symbolic Analysis and Design of Analog Active Filters"

*Examiner	Thai Phan	Date Considered	11/17/05

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

990052

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY. DOCKET NO.	SERIAL NO.
1410-794	10/628,578

APPLICANT

RUPPERT et al

FILING DATE	GROUP
July 29, 2003	Art Unit 2128

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		2001/029601	10-2001	Tomohisa et al.			

FOREIGN PATENT DOCUMENTS

		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

T.P.	International Search Report and Written Opinion mailed March 18, 2005 in corresponding PCT application PCT/IB2004/002423
T.P.	Russell, "Linear Circuit Analysis by Symbolic Algebra", 1696, pp. 573-86
T.P.	Hassoun et al, "A Hierarchical Network Approach to Symbolic Analysis of Large-Scale Networks", IEEE Transactions on Circuits and System I: Fundamental Theory and Applications, IEEE Inc., New York, vol. 42, no. 4, 1 April 1197, pp. 201-2011

*Examiner	Thai Phan	Date Considered	11/17/05

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)

953928